



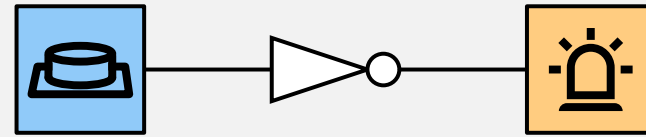
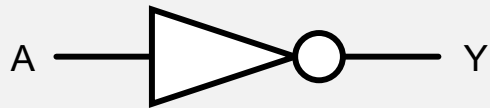
Digitale Logik 2 – Gatter



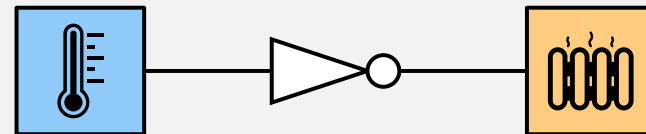


Inverter

A	Y
0	1
1	0



Kühlschrankbeleuchtung

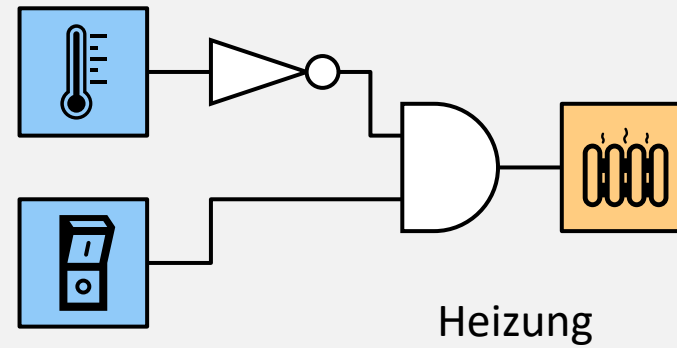
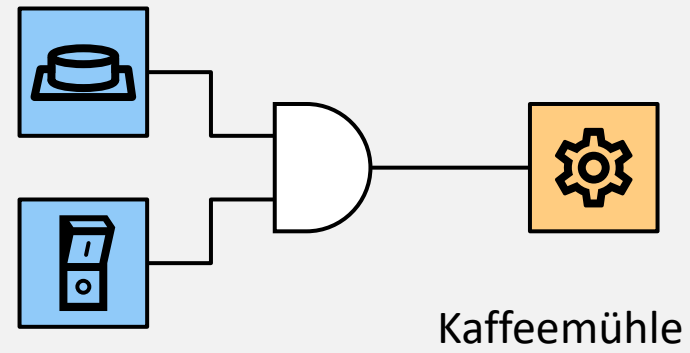
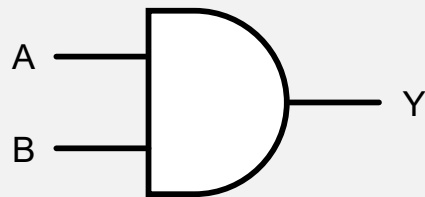


Heizung



AND-Gatter

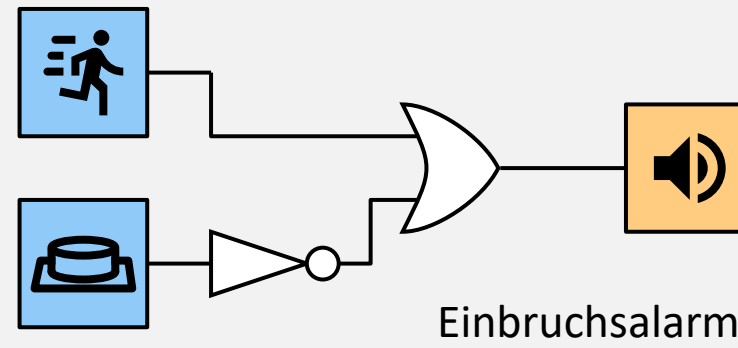
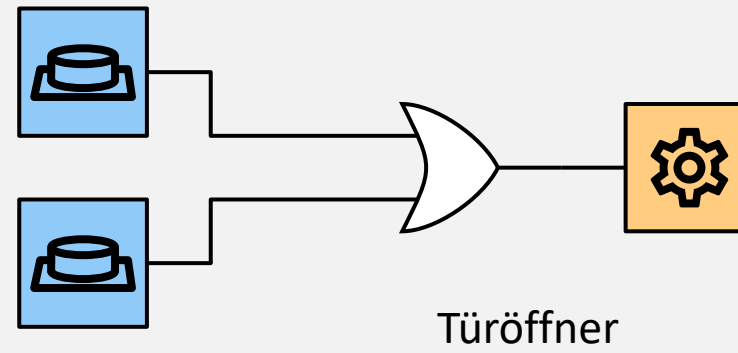
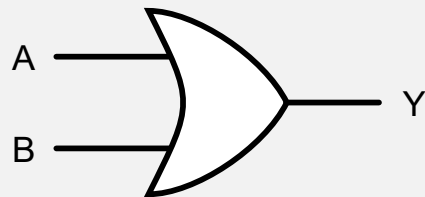
A	B	Y
0	0	0
0	1	0
1	0	0
1	1	1





OR-Gatter

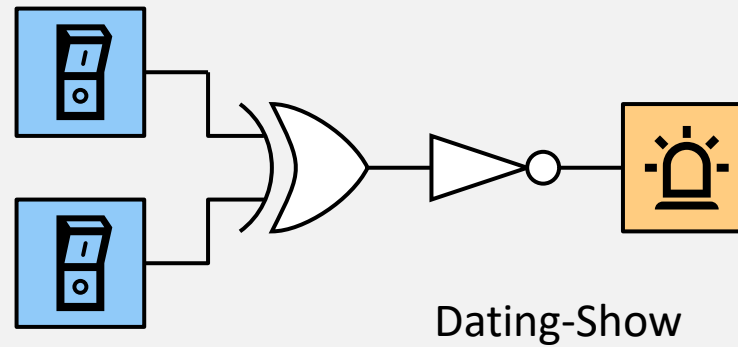
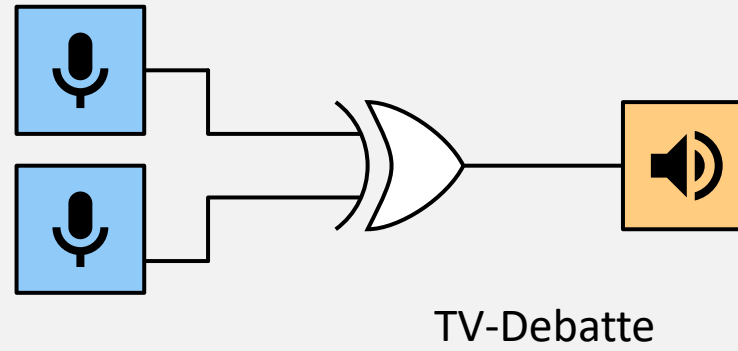
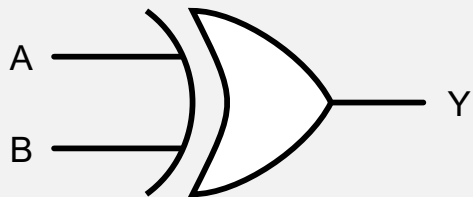
A	B	Y
0	0	0
0	1	1
1	0	1
1	1	1





XOR-Gatter

A	B	Y
0	0	0
0	1	1
1	0	1
1	1	0





NAND-Gatter

A	B	Y
0	0	1
0	1	1
1	0	1
1	1	0

